

Time		Day 1	Day 2	Day 3	Day 4
		4/23/2013	4/24/2013	4/25/2013	4/26/2013
9:00	9:15	Workshop Introduction	Programming the Xeon Phi Coprocessor (MPI, native)	OpenAcc: Lecture 1	Presentation: ARM-based systems at BSC
9:15	9:30				
9:30	9:45				
9:45	10:00	Introduction to Xeon Phi Coprocessors	Performance Analysis Methodologies	Hands-on 1	
10:00	10:15				
10:15	10:30				
10:30	10:45	Break	Break	Break	Break
10:45	11:00	Programming the Xeon Phi Coprocessor (native, offload)	Hands-on (MPI + Performance)	OpenAcc: Lecture 2	Tutorial: ARM HPC system software stack
11:00	11:15				
11:15	11:30				
11:30	11:45				
11:45	12:00				
12:00	12:15	Lunch	Lunch	Lunch	Lunch
12:15	12:30				
12:30	12:45				
12:45	13:00				
13:00	13:15	CSC Cluster Introduction	Optimization Strategies	Hands-on 2	Hands-on
13:15	13:30				
13:30	13:45	Hands-on (Programming)	Debugging (Rogue Wave)	OpenAcc: Lecture 3	
13:45	14:00				
14:00	14:15				
14:15	14:30		Outlook on OpenMP 4	Break	
14:30	14:45				
14:45	15:00	Break	Break	Break	
15:00	15:15				
15:15	15:30	Debugging on Xeon Phi Coprocessors	Outlook on OpenMP 4	Hands-on 3	Hands-on
15:30	15:45				
15:45	16:00				
16:00	16:15	Hands-on (Debugging)	Case Study	OpenAcc: Lecture 4	Wrap-up Day 4
16:15	16:30				
16:30	16:45		Overflow Buffer	Wrap-up Day 3	
16:45	17:00				
17:00	17:15	Wrap-up Day 1	Wrap-up Day 2	Day 3 ends	Day 4 ends
17:15	17:30				
17:30		Day 1 ends	Day 2 ends		